

**From:** [Moody, Dustin \(Fed\)](#)  
**To:** [Alperin-Sheriff, Jacob \(Fed\)](#)  
**Subject:** RE: Does This Work for Everyone?  
**Date:** Tuesday, February 5, 2019 10:45:00 AM

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Do we want to say something more along the lines of what you wrote, but instead of saying these are the final picks, say these two are the ones we are thinking about. Are they the right ones? I thought we wanted more feedback instead of making the choice on our own.

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**From:** Alperin-Sheriff, Jacob (Fed)  
**Sent:** Tuesday, February 05, 2019 10:41 AM  
**To:** internal-pqc <internal-pqc@nist.gov>  
**Subject:** Does This Work for Everyone?

Hi all,

In response to the feedback we have decided to suggest that teams try to focus specifically on Cortex M-4 and Artix-7 for hardware implementations for Round 2. However, we will still be considering implementations on other constrained devices (microprocessors, FPGAs, ASICs, etc.) both by teams and by third parties in our evaluations.

—Jacob Alperin-Sheriff